FIG. 1
(PRIOR ART)

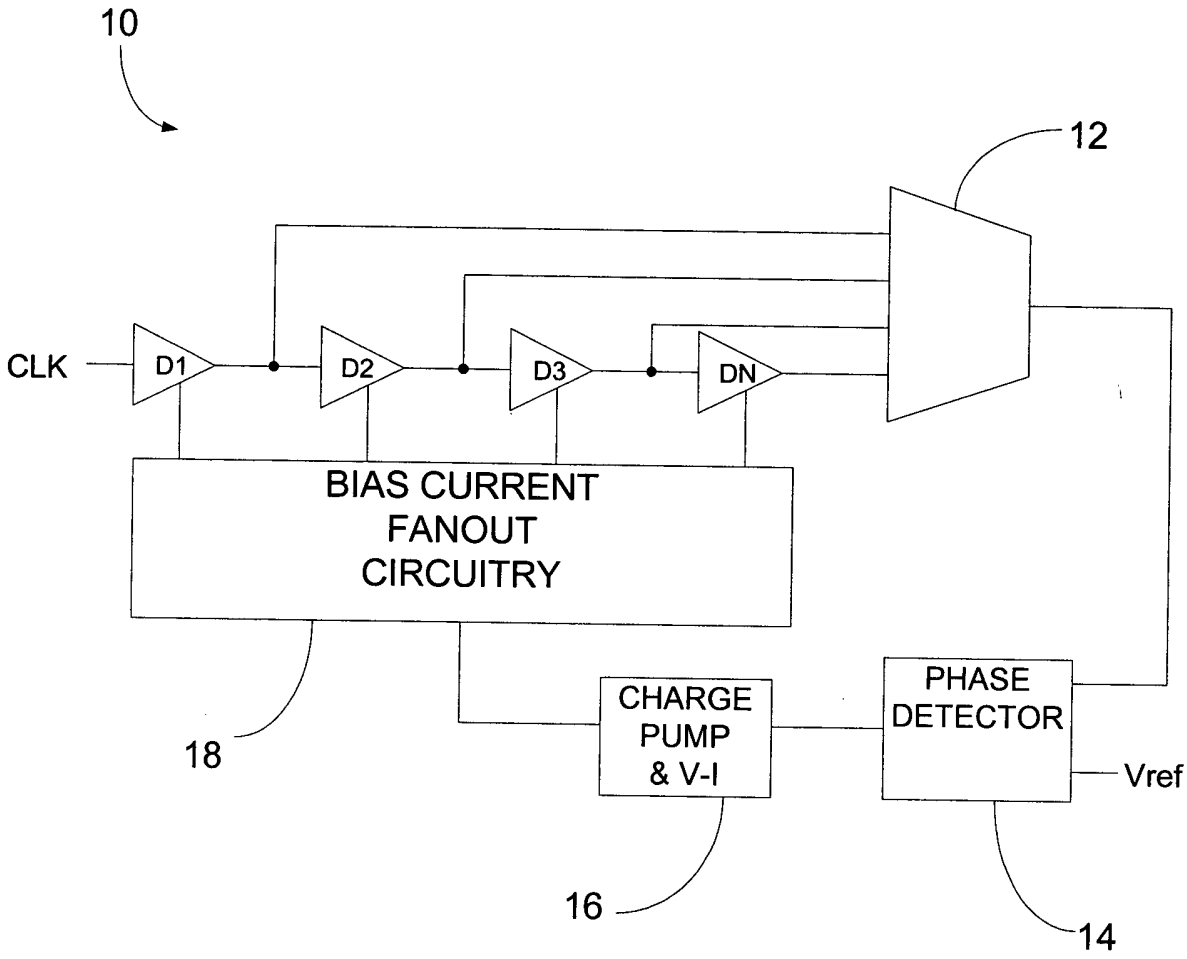


FIG. 2
(PRIOR ART)

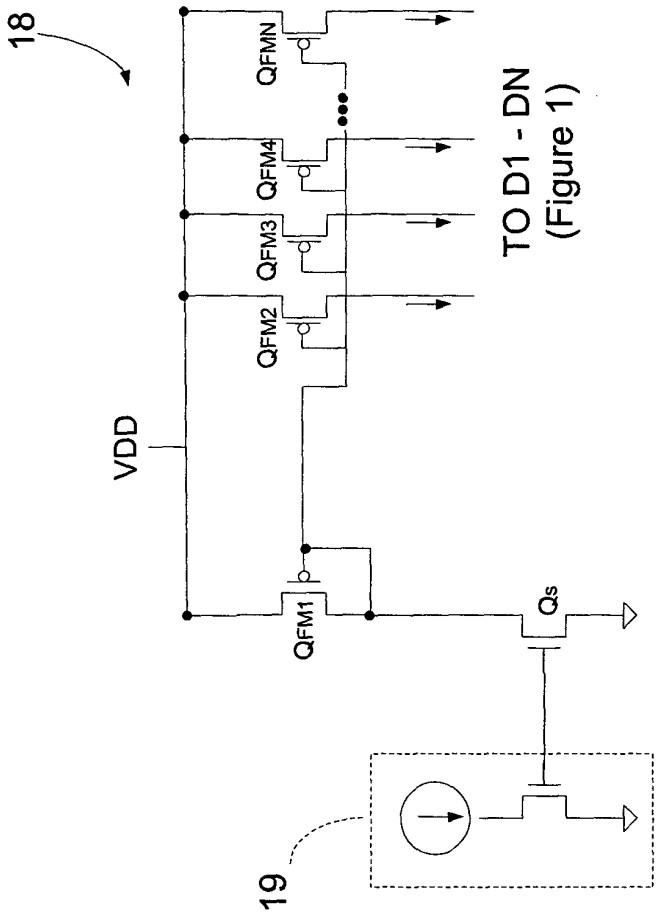


FIG. 3

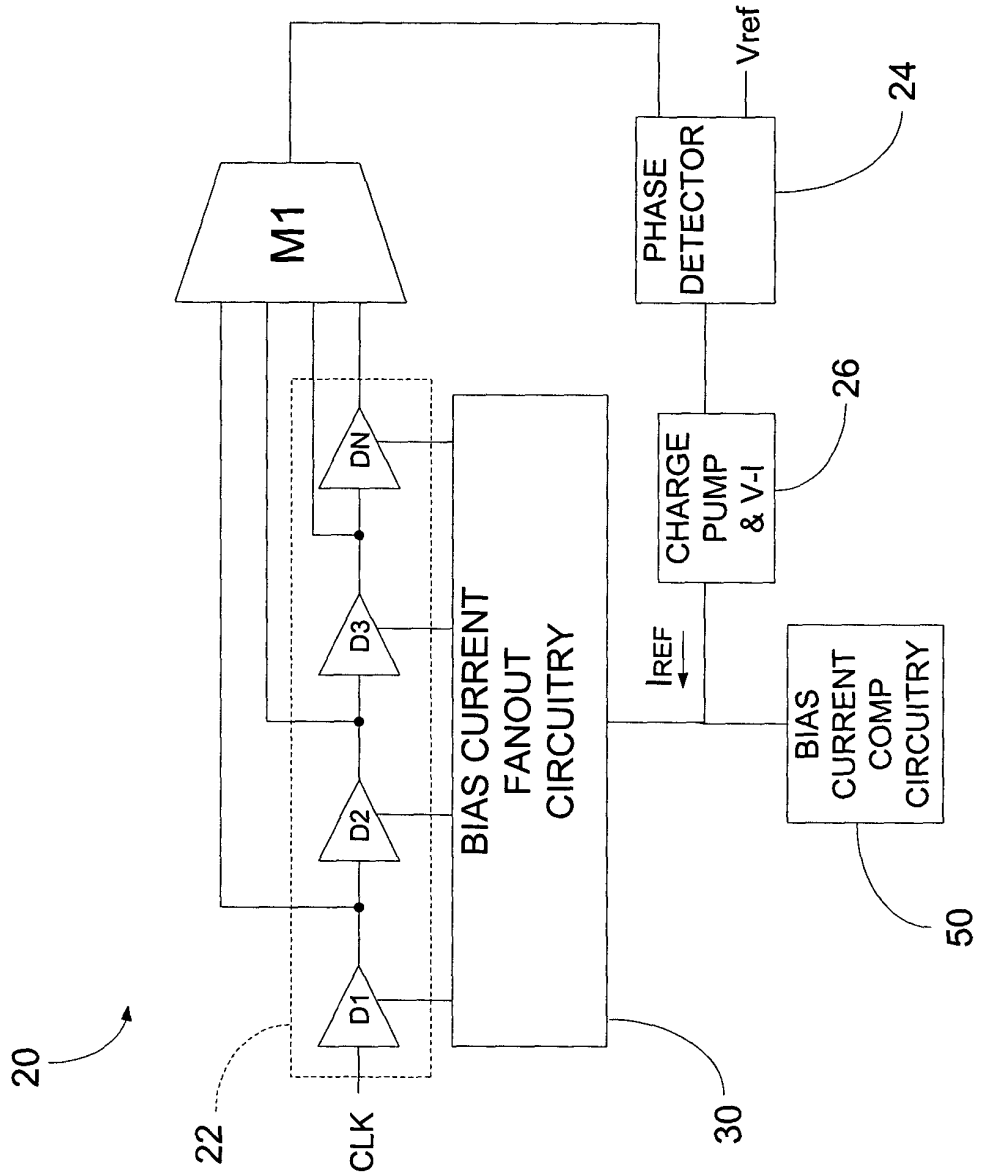
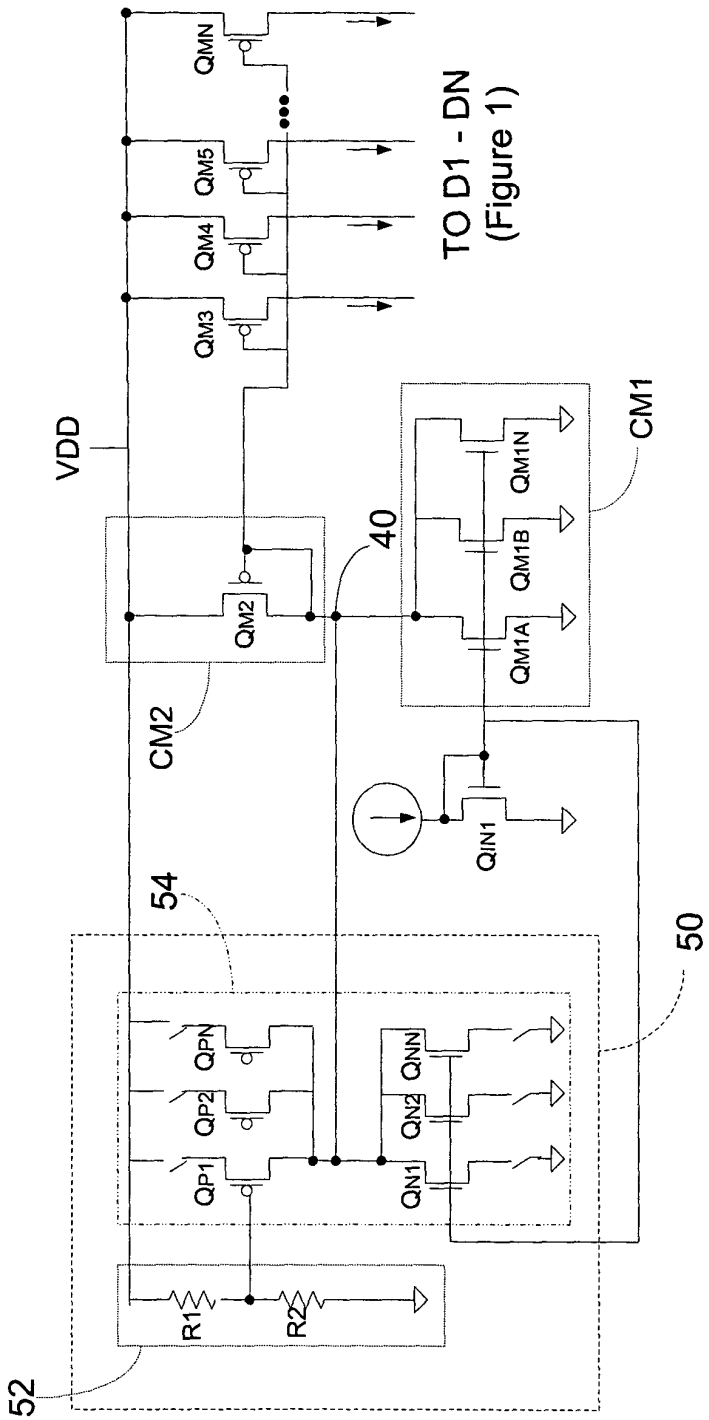

20
22
CLK
D1
D2
D3
DN
M1
BIAS CURRENT FANOUT CIRCUITRY
30
IREF
PHASE DETECTOR
Vref
24
CHARGE PUMP & V-I
26
BIAS CURRENT COMP CIRCUITRY
50

FIG. 4


VDD
QMN
QM5
QM4
QM3
TO D1 - DN
(Figure 1)
CM1
QM1N
QM1B
QM1A
40
QM2
CM2
QIN1
54
50
QPN
QP2
QP1
QNN
QN2
QN1
R1
R2
52